

ABSTRACT OF THE DISCLOSURE

An offset-compensation drive circuit turns on first, second and third switches to charge a first capacitor to an offset voltage of a drive circuit, and thereafter turns off the first and second switches and turns on a fourth switch to charge a second capacitor to a 5 first voltage loss caused by a parasitic capacitor of an input node of the drive circuit. Following this, the third and fourth switches are turned off and fifth and sixth switches are turned on. At this time as well, there occurs a second voltage loss due to the parasitic capacitor and thus an output voltage is equal to a difference between an input voltage and the second voltage loss. Supposing that the parasitic capacitor, the first 10 capacitor and the second capacitor have the same capacitance value, the second voltage loss is one-sixth as large as the first voltage loss.